



10/80/254 *Cop2*

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Farnworth et al.

Patent No.: 7,034,560 B2

Issued: April 25, 2006

For: DEVICE AND METHOD FOR
TESTING INTEGRATED CIRCUIT DICE
IN AN INTEGRATED CIRCUIT MODULE

Attorney Docket No.: 2269-2883.4US

CERTIFICATE OF MAILING

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October 4, 2006
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**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
OFFICE MISTAKES (37 C.F.R. § 1.322)**

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Sir:

It is noted that several errors appear in this patent of a typographical nature. These errors are due to mistakes in printing on the part of the U.S. Patent and Trademark Office, and occurred through no fault of the Applicants. A certificate of correction in the form attached hereto is requested.

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OCT 12 2006

Attached hereto in duplicate is Form PTO/SB/44 with at least one copy being suitable for printing.

Respectfully submitted,



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Date: October 4, 2006
JRD/csw

Enclosures: PTO/SB/44 in duplicate

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Page 1 of 1

DATED : April 25, 2006

INVENTOR(S) : Warren M. Farnworth, James M. Wark, Eric S. Nelson, and Kevin G. Duesman

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In ITEM (56) "Reference Cited,
U.S. PATENT DOCUMENTS,"

1st page, 2nd column, before 1st entry,

insert --4,451,489 9/1995 Leedy--

MAILING ADDRESS OF SENDER:

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James R. Duzan
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